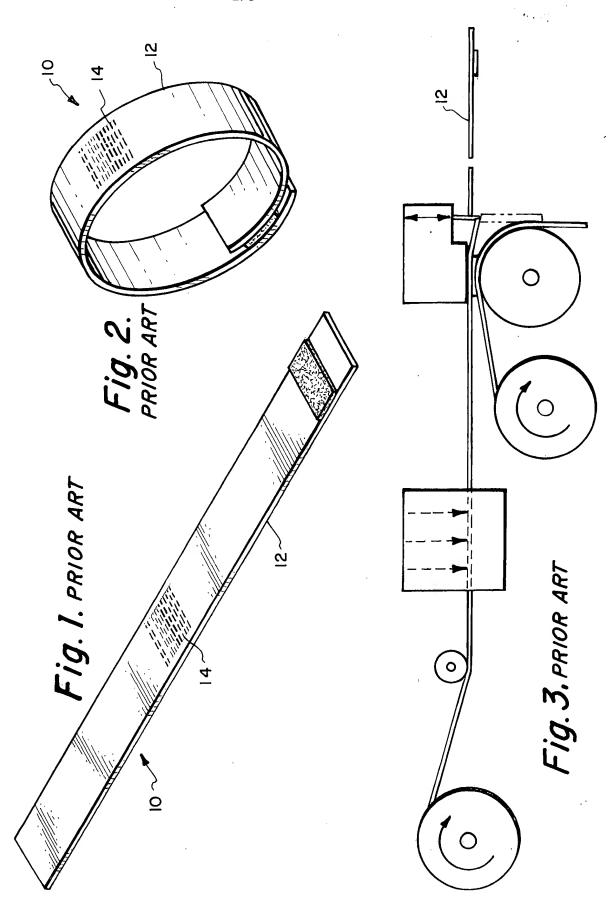
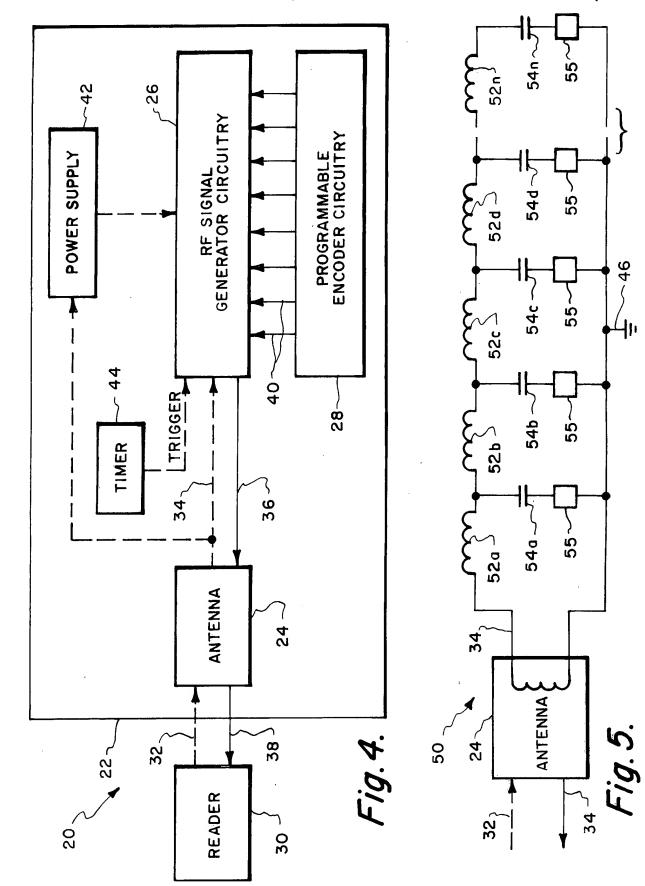


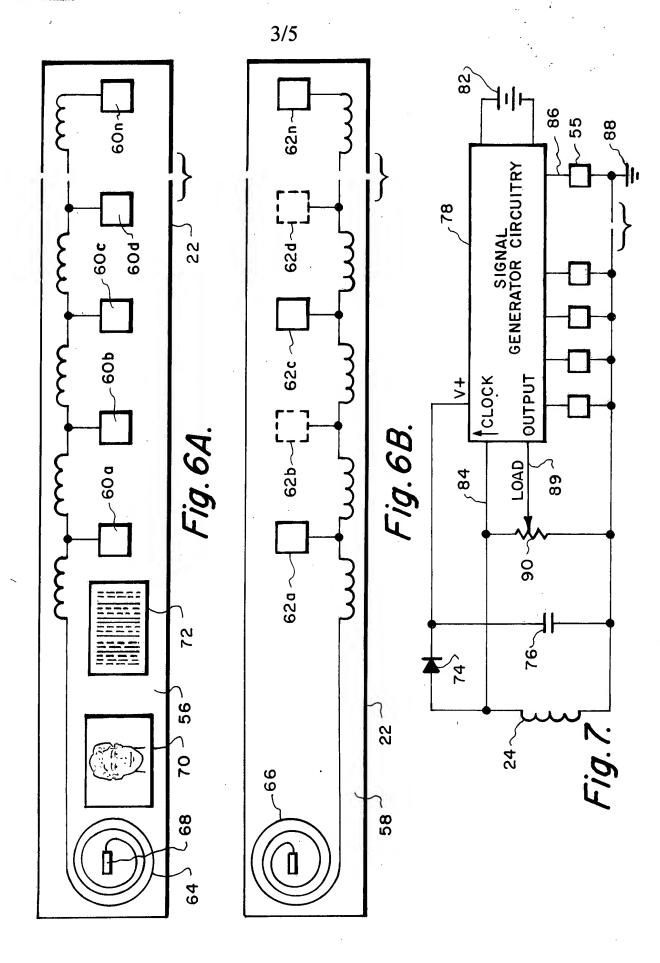
1/5



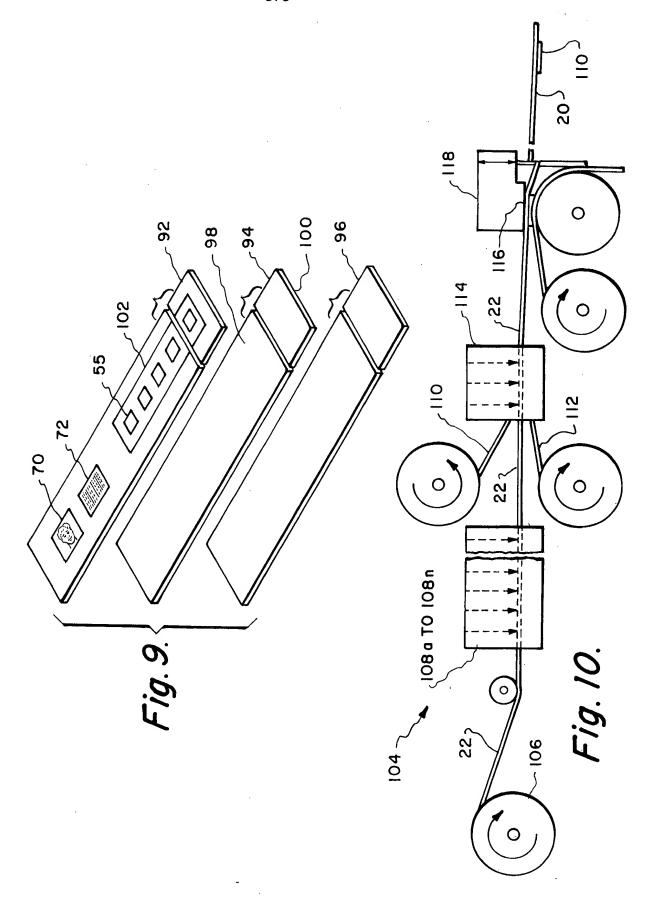
0.G. FIG. 370 572.1

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4/5 ~86 ~55 168 88 **88** 86 55 557 102 80, 80 V+ CLOCK OUTPUT OUTPUT CLOCK V+ 90 06 78₇ 787 LOAD CIRCUIT LOAD CIRCUIT \sim 68 .√ 89 -√ ₩ 92 · 767 747 74 2 24 24 Fig.88. Fig.84. Fig.8C. 92 94 7 96



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